

Design and Implementation of JPEG CODEC using NoC

MADIHA KAUSAR¹, MOHAMMED ARSHAD MURTAZA², KAUSER ANJUM³

¹Assistant Professor, Dept. of E&CE, K.C.T. Engineeering College, Kalaburagi, Karnataka, India ²M.Tech Student, VLSI & Embedded Systems, Sharnbasva University, Kalaburagi, Karnataka, India ³Ph.D, Dept. of E&CE, K.C.T. Engineeering College, Kalaburagi, Karnataka, India ***

Abstract - An image compression and decompression system using NoC structure is developed and implemented. Due to the increasing demands of image transmission in computer and mobile systems, research on image compression has increased significantly. The JPEG (Joint Photographic Experts Group) standard is a commonly used method for lossless compression of digital images, especially those produced by digital photography. Digital images require a lot of storage space. The aim of this work is to design a VLSI architecture for basic sequential JPEG encoding and decoding processes. A codec that specifies how an image is compressed into a stream of bytes and then decompressed back into an image is defined by the JPEG standard. To achieve high execution speed, the architecture makes best use of pipeline and parallelism principles. Providing effective on-chip communication networks, network-on-chip (NoC) is a new paradigm in complicated system-on-chip (SoC) designs. It enables scalable communication and decoupling of communication and computing. Data is sent over networks as packets. Data routing is primarily done through routers. The router architecture must therefore be effective, with lower latency and higher throughput; the effectiveness of the router architecture is assessed in a two-dimensional network topology. NoC is used to speed up the image transfer process and thus provide a shorter processing time for transferring data over the network. The design is implemented using Verilog HDL and simulated in Xilinx ISE Design Suite 14.7.

Key Words: Image processing, Lossy Image compression, Image decompression, JPEG, CODEC, NoC.

1. INTRODUCTION

The reason why digital representation of the signal is more reliable than its analogue counterpart for processing, manipulation, storage, recovery, and transmission over long distances, even across the globe through communication networks, is why we are talking about digital networks, digital representation of images, movies, video, TV, voice, and digital libraries today. Digital technology has only recently been used to the transmission, recording, and processing of images, but it has quickly revolutionised many areas of image processing. Because it can carry out operations that are challenging or impossible to carry out in the usual analogue format, the digital mode has largely supplanted analogue technology in various applications. So, both when they are created and when they are presented, video signals are intrinsically analogue. There is an opportunity to transform these signals into digital format in between these end points.

For effective storage and transmission, digital images must be compressed because they have a significant amount of data. The need for digital picture storage, editing, and transport has exploded with the advent of digital cameras. These image files have a tendency to be enormous and take up a lot of RAM. A typical 640 x 480 color image has about a million elements, but a 256 x 256 grayscale image has 65,536 to store. Standard broadcast television requires 100 to 200 Mbps for video applications, whereas low resolution applications like teleconferencing, remote surveillance, etc. just need a few megabits per second, when the frame rate and word size are decreased, the data rate can be brought down to a few tens of kilobits. Using picture compression methods at the source could result in even more compression. At the destination, these compressed images are decompressed for viewing and analysis.

Many picture compression methods have been developed in the recent years. These methods seek to reduce the size of the photos by sending just the non-redundant data as it is received. Other methods are inherently lossy. In this case, the reconstructed images are actually close copies of uncompressed originals. The original the and reconstructed images' corresponding pixel values are different. As a result, compression may be accomplished without significantly affecting the visual image quality. A group of picture compression mechanisms are defined by the JPEG standard. The baseline sequential encoding scheme is the mechanism that is most frequently utilised. This approach for lossy compression [1] is used.

Image compression and decompression techniques can be used to solve the digital picture transmission issue. In order to speed up the forwarding of images and provide a shorter processing time to transfer data through the network with a low latency, it can only transmit nonredundant data, as it does with NoC. Providing effective on-chip communication networks, a network-on-chip

© 2023, IRJET | Impact Factor value: 8.226 | ISO 9001:2008 Certified Journal

e-ISSN: 2395-0056 p-ISSN: 2395-0072

(NoC) is a new paradigm in complicated system-on-chip (SoC) designs [2]. Scalable communication is possible, and communication and processing can be separated.

2. NETWORK ON CHIP (NoC)

The NoC technology was created to address bus short circuits. It is a method for creating a SoC design's intercore communication subsystem for intellectual property. Dedicated buses are used between communication resources in the system-on-chip communication strategy. It does not allow for any flexibility when it comes to communication requirements, thus planning is always a need. Using public transportation is still an option, but it has the drawback of not scaling up properly as the number of resources increases. By putting in place a network of and switches, micro-routers, resources for communication. NoC is intended to address these flaws. Future ASIC design has been suggested using the NoC design paradigm. The move to NoC-based solutions is being fuelled primarily by the present VLSI chip communication design methodology's inability for deep sub-micron chip fabrication technology. Technology scaling's detrimental effects on global connections, a higher reliance on fault mechanisms as feature 6 size reduces, and a growth in the use of parallel architectures are some of the factors contributing to NoC's rising popularity.

The manufacture of such integrated chips presents a number of design issues for NoC-based system-on-chips. First, a target NoC topology that complies with the design limitations and performance specifications. Second, the physical connection mechanisms for data transfer between processor cores are provided by the design of network interfaces to access the on-chip network and routers. The third consideration is selecting communication protocols appropriate for chips interconnect networks. Finally, the network of chips for the future will grow increasingly sensitive and prone to mistakes and failures as the magnitude and speed of technological progress rise. When communicating via chips, fault tolerance becomes crucial. For reduced wire routing, easier timing shutdown, higher running frequencies, and quick IP address changes, today's SoCs need on-chip network IP connectivity. System-on-chip success for next embedded applications depends on network-on-chips technology.

2.1 NoC ARCHITECTURE

The general architecture of the NoC is depicted in Figure 1. Processor Elements (PE), Network Interfaces (NI), Routers (R), and Channels make up this system. Each PE has a network interface that connects it to a nearby router (NI). WE establish a logical connection between the network and the IP core. The front end and the back end can be separated into two components. The UI handles requests to the IP core and is not aware of the network. The back end, which manages network protocol, sorts and arranges packets, buffers, and aids the router with storage, is directly connected to the network. Data packets are transmitted between the source PE and the destination PE. During transmission, the packet is transmitted over the network hop by hop in accordance with the router's choice.

Switches and a buffer are also components of a router. Here, store and forward buffering is applied. A router and a related link make up a node. Links allow for data transmission on the network by transferring packets between routers. According to the chosen topology, it consists of a number of wires connecting various router networks; in this case, a 33 2D-Mesh NoC structure is employed. A router is in charge of transferring data utilising particular routing algorithms and control flow mechanisms from source to destination.

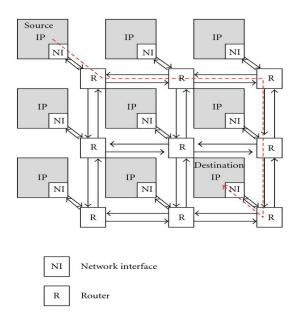


Fig- 1: General Architecture of NoC

3. PROPOSED METHOD

Figure 2 displays a block diagram of a JPEG codec with NoC. There are various processes involved in the encoding process.



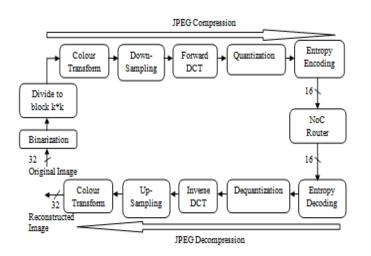


Fig- 2: Block diagram of the JPEG Codec with NoC

3.1 Encoding

1. Conversion of the image's color space: The image should first be transformed from RGB to Y'CBCR (or informally YCbCr). There are three parts to it: Y', CB, and CR. Y' represents the brightness of the pixel, while CB and CR represent the colour (divided into blue and red components). The colour space utilised in digital colour television and digital video, including DVD video, is roughly the same. Higher compression is possible thanks to the Y'CBCR colour space conversion without considerably lowering the observed image quality.

2. Downsampling: People perceive image detail in image brightness (Y' component) substantially more clearly than in image hue and saturation due to the density of colour and light sensitive receptors in the human eve (Cb and Cr components). Encoders can be made to compress images more effectively using this information. Reducing the spatial resolution of the Cb and Cr components is a common next step when converting to the Y'CBCR colour model (referred to as "downsampling" or "color subsampling"). JPEG images are often downscaled at ratios of 4:4:4 (no down-processing), 4:2:2 (by a factor of 2 horizontally), or (most frequently) 4:2:0 (by a factor of 2 both horizontally and vertically). The remaining press treats Y', Cb, and Cr differently but quite similarly.

3. Block division: Each channel must be divided into 88 blocks after subsampling. This results in minimum coded units (MCU) blocks of size 8 8 (4:4:4 - no subsampling), 16 8 (4:2:2), or most frequently 16 16, depending on the colour subsampling (4:2:0). Macroblocks are what MCUs are known as in video compression. If the channel data does not represent an integer number of blocks, the encoder must insert some sort of dummy data into the remaining empty block region. Filling edges with a solid colour (like black) may result in ringing artefacts in the

border's visible portion. Such artefacts can be reduced (but not always entirely eliminated) via edge pixel rendering, and more advanced edge-filling techniques can also be applied.

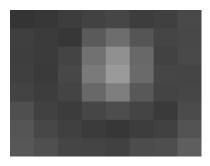


Fig- 3: 8x8 sub-image in grayscale with 8 bits

4. Discrete Cosine Transform (DCT): Next, a normalised two-dimensional Type II Discrete Cosine Transform is used to transform each 8 8 block of each component (Y, Cb, Cr) into a frequency domain representation (DCT). One such 88 8-bit subimage might be, for instance:

52	55	61	66	70	61	64	73]
63	59	55	90	70 109 144	85	69	72
62	59	68	113	144	104	66	73
63	58	71	122	154	106	70	69
67	61	68	104	126	88	68	70
79	65	60	70	77 55 65	68	58	75
85	71	64	59	55	61	65	83
87	79	69	68	65	76	78	94

An 8 x 8 block's values are shifted from the positive range to zero-centered before the DCT is calculated. Each starting block entry for an 8-bit picture falls between the [0, 255] range. The amended range is [-128, 127], and it is obtained by subtracting the midpoint of the range (in this case, the value 128) from each input. The subsequent level of DCT processing will require less dynamic range thanks to this step. These values are obtained as a result of this step:

				3	r				
				_	\rightarrow				
	$\begin{bmatrix} -76 \\ -65 \\ -66 \\ -65 \\ -61 \\ -49 \\ -43 \\ -41 \end{bmatrix}$	-73	-67	-62	-58	-67	-64	-55	
	-65	-69	-73	-38	-19	-43	-59	-56	
a —	-66	-69	-60	-15	16	-24	-62	-55	1
g =	-65	-70	-57	-6	26	-22	-58	-59	$\downarrow^{g.}$
	-61	-67	-60	-24	$^{-2}$	-40	-60	-58	
	-49	-63	-68	-58	-51	-60	-70	-53	
	-43	-57	-64	-69	-73	-67	-63	-45	
	-41	-49	-59	-60	-63	-52	-50	-34	

© 2023, IRJET The two-dimensional DCT is the following phase, which results in:

$$G_{u,v} = rac{1}{4} lpha(u) lpha(v) \sum_{x=0}^7 \sum_{y=0}^7 g_{x,y} \cos iggl[rac{(2x+1)u\pi}{16} iggr] \cos iggl[rac{(2y+1)v\pi}{16} iggr]$$

u is the horizontal spatial frequency of the numbers 0 through u plus eight. The integers 0 through v have a vertical spatial frequency of v. The normalisation scale factor that makes the transformation orthonormal is (u) = 1/(2)1/2, if u = 0; 1 otherwise. The pixel value in (x,y) coordinates is gx,y. The DCT coefficient in coordinates is Gu,v (u, v). If we apply this transformation to the aforementioned matrix, we obtain the values shown below (rounded to two decimal places): Take notice of the rather large entry in the upper left corner. The base height of the whole block is determined by the DC coefficient, commonly known as the constant component. AC coefficients make up the remaining 63 coefficients (also called AC components).

				u					
				\rightarrow					
	-415.38	-30.19	-61.20	27.24	56.12	-20.10	-2.39	0.46	
	-415.38 4.47	-21.86	-60.76	10.25	13.15	-7.09	-8.54	4.88	
<i>c</i> –	-46.83	7.37	77.13	-24.56	-28.91	9.93	5.42	-5.65	
G =	-46.83 -48.53 12.12	12.07	34.10	-14.76	-10.24	6.30	1.83	1.95	↓ ^{v.}
	12.12	-6.55	-13.20	-3.95	-1.87	1.75	-2.79	3.14	
	-7.73	2.91	2.38			0.94		1.85	
	-1.03 -0.17	0.18	0.42	-2.42	-0.88	-3.02	4.12	-0.66	
	-0.17	0.14	-1.07	-4.19	-1.17	-0.10	0.50	1.68	

The benefit of DCT is that, as can be seen above, it tends to concentrate the majority of the signal in one corner of the result. By lessening the overall size of the DCT coefficients, the subsequent quantization step emphasises this effect and creates a signal that is simple to effectively compress in the entropy phase.

5. Quantization: The human eye is not very good at differentiating the intensity of high-frequency brightness variations, but it can detect very minor differences in brightness across a very big region. As a result, the information from high-frequency components can be reduced. To achieve this, just divide each component of the frequency range by its respective constant, rounding the result to the nearest whole number. If the DCT calculation is carried out with appropriate high accuracy, this rounding is the sole lossy operation in the entire process (apart from colour subsampling). As a result, many higher frequency components are frequently

rounded to zero and many others are transformed into tiny positive or negative values, which require a lot less bits to represent. Higher values for the quantization matrix's constituent parts lead to greater compression. According to the original JPEG standard, a typical quantization matrix is as follows:

	[16	11	10	16	24	40	51	61	
Q =	12	12	14	19	26	58	60	55	
	14	13	16	24	40	57	69	56	
0-	14	17	22	29	51	87	80	62	
Q –	18	22	37	56	68	109	103	77	
	24	35	55	64	81	104	113	92	
	49	64	78	87	103	121	120	101	
	72	92	95	98	112	100	103	99	

Calculated are the quantized DCT coefficients.

$$B_{j,k} = ext{round}\left(rac{G_{j,k}}{Q_{j,k}}
ight) ext{ for } j = 0, 1, 2, \dots, 7; k = 0, 1, 2, \dots, 7$$

where Q is the higher quantization matrix, B is the quantized DCT coefficients, and G are the unquantized DCT coefficients. Combining the above DCT coefficient matrix with this quantization matrix results in:

	-26	$^{-3}$	$^{-6}$	2	2	$^{-1}$	0	0]
	0	$^{-2}$	-4	1	1	0	0	0
	$ \begin{bmatrix} -26 \\ 0 \\ -3 \\ -3 \\ 1 \end{bmatrix} $	1	5	$^{-1}$	$^{-1}$	0	0	0
P _	-3	1	2	$^{-1}$	0	0	0	0
<i>D</i> –	1	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0

For instance, rounding to the next whole number and using the DC factor of -415

round
$$\left(\frac{-415.37}{16}\right) =$$
round $(-25.96) = -26.$

Be aware that the majority of the subblock's high-frequency elements (those with a spatial frequency of x or y greater than 4) are filled with zero values.

6. Entropy coding: This particular type of lossless data compression uses entropy coding. The run-length encoding (RLE) approach, which groups frequencies with a similar frequency together, adds zeros to the length

encoding, and then applies Huffman encoding to the remaining bits, is used to arrange the image components in a "zigzag" pattern.

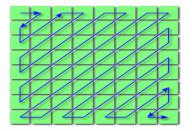


Fig- 4: Zigzag arrangement of JPEG image components

3.2 NoC Router

The NoC takes compressed image data as input, its function is to speed up the image transfer process and thus provide shorter processing time for low-latency data movement in the network.

3.3 Decoding

There are various steps involved in decoding: All of the aforementioned stages are performed in reverse to display the image during decoding. Consider the DCT coefficient matrix (after adding the DC coefficient difference)

-26	$^{-3}$	$^{-6}$	2	2	$^{-1}$	0	0	
0	$^{-2}$	-4	1	1	0	0	0	
$^{-3}$	1	5	$^{-1}$	$^{-1}$	0	0	0	
$^{-3}$	1	2	$^{-1}$	0	0	0	0	
1	0	0	0	0	0	0	0	
0	0	0	0	0	0	0	0	
0	0	0	0	0	0	0	0	
0	0	0	0	0	0	0	0	

then using the quantization matrix and the input from the aforementioned result

-416	-33	-60	32	48	-40	0	0]
0	-24	-56	19	26	0	0	0
-42	13	80	-24	-40	0	0	0
-42	17	44	-29	0	0	0	0
18	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0
L 0	0	0	0	0	0	0	0

This closely resembles the upper-left corner of the original DCT coefficient matrix. The two-dimensional inverse DCT

(2D Type III DCT), which is the following step, is obtained by:

$$f_{x,y} = rac{1}{4} \sum_{u=0}^7 \sum_{v=0}^7 lpha(u) lpha(v) F_{u,v} \cos iggl[rac{(2x+1)u\pi}{16}iggr] \cos iggl[rac{(2y+1)v\pi}{16}iggr]$$

where x is an array of integers from 0 to 8. The integers 0 through 8 make up the pixel column y. For numbers 0 through u and up to 8, (u) is defined as above. In coordinates, Fu,v reconstructed the approximation coefficient (u, v). The value of the reconstructed pixel in (x, y) coordinates is fx,y. As the original contained integers, rounding the result to integers yields the numbers in the illustration (delimited by 128)

[−66	-63	-71	-68	-56	-65	-68	-46]
-71	-73	-72	-46	-20	-41	-66	-57
-70	-78	-68	-17	20	-14	-61	-63
-63	-73	-62	$^{-8}$	27	-14	-60	-58
-58	-65 -57	-61	-27	-6	-40	-68	-50
-57	-57	-64	-58	-48	-66	-72	-47
-53	-46	-61	-74	-65	-63	-62	-45
-47	-34	-53	-74	-60	-47	-47	-41

then multiplying each entry by 128

62	65	57	60	72	63	60	82]	
57	55	56	$\frac{60}{82}$	108	87	62	71	
58	50	60	111	148	114	67	65	
65	55	66	120	155	114	68	70	
70	63	67	101	122	88	60	78	
71	71	64	70	80	62	56	81	
75	82	67	54	63	65	66	83	
81	94	75	$54 \\ 54$	68	81	81	87	

Here you may view the decompressed subimage. Generally speaking, the decompression process may produce values outside of the initial input range of [0, 255]. In this situation, the decoder must trim the output values to keep them within this range in order to prevent overflow while maintaining the decoded image's original bit depth. By subtracting the original from the uncompressed picture, the original subimage and the decompressed subimage can be contrasted (see also the images on the right), resulting in the incorrect set of values shown below:



International Research Journal of Engineering and Technology (IRJET)Volume: 10 Issue: 05 | May 2023www.irjet.net

e-ISSN: 2395-0056 p-ISSN: 2395-0072

$$\begin{bmatrix} -10 & -10 & 4 & 6 & -2 & -2 & 4 & -9 \\ 6 & 4 & -1 & 8 & 1 & -2 & 7 & 1 \\ 4 & 9 & 8 & 2 & -4 & -10 & -1 & 8 \\ -2 & 3 & 5 & 2 & -1 & -8 & 2 & -1 \\ -3 & -2 & 1 & 3 & 4 & 0 & 8 & -8 \\ 8 & -6 & -4 & -0 & -3 & 6 & 2 & -6 \\ 10 & -11 & -3 & 5 & -8 & -4 & -1 & -0 \\ 6 & -15 & -6 & 14 & -3 & -5 & -3 & 7 \end{bmatrix}$$

with a 5 numbers per pixel absolute error on average.

$$rac{1}{64}\sum_{x=0}^7\sum_{y=0}^7|e(x,y)|=4.8750$$

The lower left corner, where it grows darker than the pixel on the right, is where the inaccuracy is most obvious.

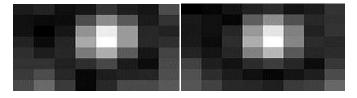
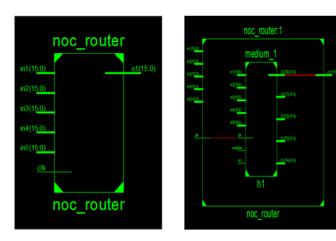


Fig- 5: Small differences can be seen between the original (left) and the compressed image (right), best seen in the lower left corner.

4. PERFORMANCE ANALYSIS

Verilog is used to build the design, and Xilinx ISE Design Suite 14.7 is used to simulate it.



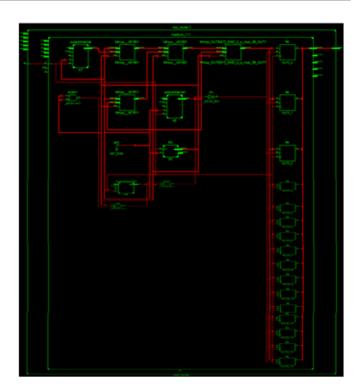


Fig- 6: RTL View of NoC router and its top level schematic

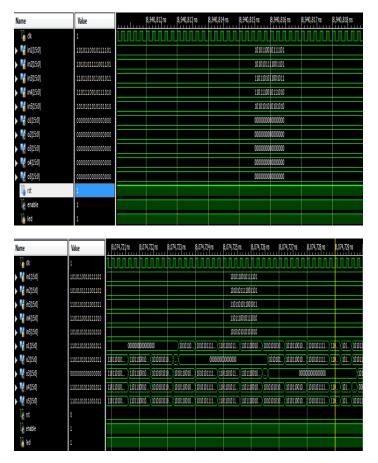


Fig- 7: Simulation Results



International Research Journal of Engineering and Technology (IRJET)e-ISSN: 2395-0056Volume: 10 Issue: 05 | May 2023www.irjet.netp-ISSN: 2395-0072

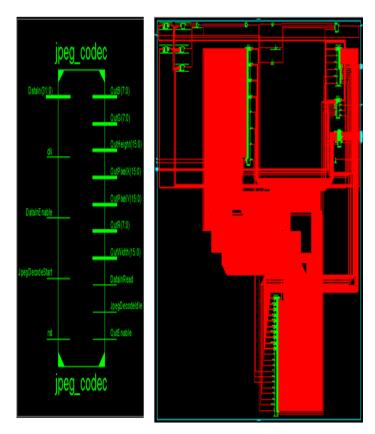


Fig- 8: RTL View of JPEG CODEC and its top level schematic

Name	Value		3,899,351,	500 ps	3,899,3	152,000 ps	3,85	9,352,500 ps	3,899,3	153,000 ps	3,8	19,353,500 ps	3,899,354,00) ps	3,899,354,	500 ps	3,899,35	55,000 ps
l _e rst	1																	
l _e cik	1			\Box														
🕨 👫 Datain(31:0)	101010111100110110									10101	111	100110110101	1111001101					
la DatainEnable	1																	
🖥 DatainRead	1																	
🔓 JpegDecodeStart	0																	
🔓 JpegDecodeldle	0																	
Un OutEnable	0																	
OutWidth[15:0]	00000000000000000										0	000000000000000000000000000000000000000	00					
🕨 🕌 OutHeight(15:0)	00000000000000000										0	000000000000	00					
🕨 🕌 OutPixel((15:0)	000000000000000000										0	00000000000000	00					
Utrivel/[15:0]	00000000000000000										0	00000000000000	00					
🕨 🕌 OutR(7:0)	XXXXXXXXXX											X000000X						
🕨 🕌 OutG[7:0]	XXXXXXXXX											0000000						
🕨 🕌 OutB(7x0)	XXXXXXXXX											0000000						
🕨 👹 JpegData[31:0]	101010111100110110)(11	0110110	10101	01111	11001101	0_	1010101111	000	110011011	0	1010101111	1100110110	/10	0101111	000_	1100110	0110_/1
JpegDataEnable	1																	

Fig- 9: Simulation Result

5. CONCLUSIONS

The design and implementation of an image compression and decompression system utilising the NoC structure in Verilog HDL was effective. Thus, it provides faster data transmission through the network and is used to quicken the forwarding of images. Future development will involve expanding router architectures and building a productive NoC. The NoC's FPGA version will also be done. On comparing with other different compression techniques, came to a conclusion that JPEG image compression and decompression algorithm is an efficient technique for to some extent.

REFERENCES

[1] Udaya Kumar H, Madhu B C, "Design and Implementation of Lossless Data Compression Coprocessor using FPGA" International Journal of Engineering Research & Technology (IJERT) ISSN: 2278-0181 IJERTV4IS05083 Vol. 4 Issue 05, May-2015.

[2] Muhammad Athar Javed Sethi, Fawnizu Azmadi Hussin, Nor Hisham Hamid, " Survey of Network On Chip Architectures" Department of Electrical & Electronic Engineering, Universiti Teknologi PETRONAS, Tronoh, Perak, Malaysia, Sci.Int.(Lahore),27(5),4133-4144,2015.

[3] Moh'dAli Moustafa Alsayyh , Prof. Dr. Dzulkifli Mohamad &. Waheeb abu-ulbaa, "Image Compression Using Discrete Cosine Transform and Discrete Wavelet Transform" Journal of Information Engineering and Applications ISSN 2224-5782 (print) ISSN 2225-0506 (online) Vol.3, No.11, 2013.

[4] S. Deb, et al., "CMOS Compatible Many-Core NoC Architectures with Multi-Channel Millimeter-Wave Wireless Links", Proceedings of Great Lakes Symposium on VLSI (GLSVLSI), 3rd -4th May 2012.

[5] M. Bechtel Brabi and Dr. A. Rajalingam., "Recent survey for Bi-Directional network on chip pipelined architecture" International journal of Advanced Research in computer science and Software Engineering, Volume-2, issue-12, December 2012.