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A DC-DC Converter with High Voltage Gain for Motor Applications Using Fuel Cell

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Abstract - In a fuel cell vehicle, the output voltage of the fuel cell source is typically much lower than the voltage required by the system. A new DC-DC boost converter with a ideal input range and high voltage gain is proposed to act as the required power interface, which reduces the voltage stress across the system. The converter was measured as 95.01% efficiency. A DC motor is connected across the output side and closed loop is performed. Simulation is performed using MATLAB software and simulation result are shown. A simple prototype is shown in open loop .It is mainly used for motor applications and in industrial areas. The output of the motor is connected to PI controller and closed is performed. The closed loop is performed and the motor load does not vary in speed Fuel cell is used in project because it is renewable form of energy .The fuel cell uses hydrogen as fuel which is easily available and the byproduct of the Fuel cell is water .It is far convenient and does not wide open to environment. The usage of Fuel cell in the project opens wide range usage of fuel cell.

Key Words: Boost DC-DC converter, Fuel cell Vehicles, High voltage-gain, Switched-capacitor, Wide input range.

1.INTRODUCTION

As non-renewable resources such as oil, gas and coal become scarce, more and more research is focused on the problem of high energy usage and society's dependence on fossil fuels. Additionally, the number of automobiles continues to increase in most countries, causing a significant rise in air pollution. Vehicles powered by fuel cell sources may help to reduce transport's dependence on oil, and reduce polluting emissions. The fuel cells can utilize hydrogen or natural gas, to achieve a high energy density and can potentially generate "clean" electricity with high efficiency.

However, unlike batteries which have a fairly constant output voltage, the output voltage of fuel cells drops significantly with an increase of output current. Hence, a step-up DC-DC converter with a wide range of voltage-gain is

essential to interface between the low voltage fuel-cell source and the high voltage DC bus of the motor drive inverter. The conventional DC-DC Boost converters one of the most commonly used topologies for stepping up voltage. In theory, when the duty cycle approaches unity, the conventional boost converter can achieve a high voltage gain. However, it is difficult to implement a high voltage gain (e.g. more than 6), due to the existence of parasitic elements (stray inductance, capacitance) and the extreme duty cycle required. In addition, the power semiconductors suffer from a high voltage stress - the DC bus voltage. In order to obtain a DC-DC Boost converter with a high voltage gain and a low voltage stress, many different topologies have been proposed by researchers.

These converters can be divided into two types: isolated and non-isolated converters. Isolated converters are widely used in many applications, and an arbitrarily high voltage-gain can be theoretically achieved by increasing the turns ratio of the transformer employed. However, there are many situations where galvanic isolation is unnecessary, and the snubber circuit required in an isolated topology will increase the complexity of the converter design. Compared with isolated converters, the cost and magnetic losses of non-isolated converters are lower. A high voltage-gain can be achieved by introducing a coupled inductor to topology, and the converter can maintain a low device voltage stress. However, a large number of inductors is required leading to an increased volume, a higher cost, and a reduced efficiency. Non-coupled inductor-based converters can also be used to obtain a high voltage-gain reducing the number of magnetic devices.

The conventional quadratic DC-DC boost converter in can obtain a high voltage-gain, but the voltage stress across the high side power semiconductors is as high as the output-voltage. To solve this problem, the switchedcapacitor (SC) configurations introduced in, and are able to obtain a high voltage gain, but they cannot achieve flexible voltage regulation unless they are combined with other DC-DC converters. A topology called the "switched-capacitorbased active-network" (SC-ANC) is presented in; the voltage



stress across the power semiconductors can be reduced by half, and the voltages across the output capacitors can also balance themselves naturally.

However, the power switches may see a large voltage spike as a result of the leakage inductance of the circuit. The switched-capacitor circuit was studied in : it achieves flexible voltage regulation by combining it with other DC-DC converters, however the difference in potential between the ground points of the input voltage source side and the load side is a high frequency PWM voltage, because instead of a common ground structure, there is a diode located between the ground points of the input voltage source side and the load side. As a result, it may introduce issues associated with du/dt and these may limit its applications. The Z source DC-DC Boost converter has the potential for a high voltage gain. A Z source DC-DC converter with a cascaded switched-capacitor has been presented in. This topology can improve the voltage gain of the Z source DC-DC Boost converter by using the voltage multiplier function of the switched-capacitor. However, the drawbacks of the converter are obvious, such as the penalty of the discontinuous input current and the different ground points between the input voltage source side and the load side. Moreover, the power semiconductors will see a high voltage stress when the duty cycle approaches zero.

In a similar way, switched-inductor (SL) techniques can also be used in dc-dc converters to achieve a high voltage gain as presented in, and, but they often need large numbers of inductors. Therefore, the volume and cost of these converters will be increased. To address these issues, a new non-isolated high ratio step-up dc-dc converter is proposed in this Thesis, which has the following features:

 It reduces the voltage stress across the power devices and has a common ground between the input and output sides.
The two power switches turn on and off simultaneously. As a result, the control of the converter is simple, and power switches with low on-state resistance can be employed.

3) The system operates with a high voltage gain and a wide input voltage range and does not use any extreme values for its duty cycle.

This paper is organized as follows: In Section II, the configuration and operating principles of the proposed converter are presented. The voltage gain is analyzed in Section III. In Section IV, the voltage and current stresses are calculated. The design of the components is presented in Section V and in Section VI, the dynamic modeling is established. Experimental results and analysis are presented in Section VII to validate the features of the proposed converter.

2. EXISTING METHODOLGY

Since the voltage of the fuel cell source is very low and the high voltage DC bus of the motor drive inverter. The existing DC-DC converter produces variable output voltage with respect to low voltage input from fuel cell and it cannot be used for motor loads. The increasing load drops the efficiency of the fuel cell. It has no common ground, high voltage stress. The Existing or proposed converter uses only resistive load and it.

3. OPERATING PRINCIPAL OF PROPOSED CONVERT<u>ER</u>



Fig -1: Block Diagram of Proposed System

3.1 Configuration of the proposed converter

The high voltage gain DC-DC Boost converter is shown in Fig. 1. It comprises two active power switches (Q1 and Q2), five power diodes (D3-D7), two inductors (L1 and L2) and five capacitors (C1-C5). The fuel-cell source Uin and the inductor L1 are connected in series to charge capacitors C1 and C2 in parallel.

Inductor L2 is another energy storage component which is used to realize a high voltage gain. The ladder type voltage multiplier (capacitors C3-C5 and diodes D5-D7) can improve the voltage-gain further and reduces the voltage stress across the power semiconductors on the high voltage side.



Fig -2: Topology of Proposed Converter **3.2 Operating principles of the proposed converter**

The gate signals of the two power switches (Q1, Q2) are identical - Q1 and Q2 are turned on and off simultaneously. Therefore, there are two switching states in each switching period, which are shown in Fig. 3.

Switching state 1. As shown in Fig. 3(a), Q1 and Q2 turn on, L1 is charged by the DC source Uin (i.e. Uin-L1-Q1), and L2 is charged by C1 and C2 in series (i.e. C1-L2-Q2-C2-Q1). Meanwhile, C3 is charged by C2 and C4 in series (i.e. C4-D6-C3-Q2-C2-Q1).

Switching state 2. As shown in Fig. 3(b), Q1 and Q2 turn off, C1 and C2 are charged in parallel by the DC source and L1 (i.e. Uin-L1-D3-C1, and Uin-L1-C2-D4). At the same time, C4 is charged by the DC source, L1, and L2 in series (i.e. Uin-L1-D3-L2-D5-C4). In addition, C4 and C5 are charged by

the DC source, L1, L2, and C3 (i.e. Uin-L1-D3-L2-C3-D7-C5-C4), as well as through the load R. The output-voltage Uo is equal to the total voltages across C4 and C5.



Fig -3(a): Switching states of the proposed converter - Switching state 1.



Fig -3(b): Switching states of the proposed converter - Switching state 2.

According to the key operating waveforms of the proposed converter shown in Fig. 4, the inductor currents *i*L1 and *i*L2 have the same energy transfer process. When S=1, power switches Q1, Q2 and diode D6 are turned on. The current *i*Q1 increases linearly while *i*Q2 and *i*D6 decreases linearly. The output capacitor current *i*C5 is negative which means *C*5 is discharged. When S=0, power switches Q1, Q2 and diode D6 are turned off. The currents *i*D5 and *i*C5 decrease linearly. The capacitor voltage fluctuations reflect the charging and discharging processes. It can be seen from the capacitor voltages *U*C2 and *U*C3 that capacitors *C*2 and *C*3 have the opposite charging and discharging states.

4. STEADY-STATE ANALYSIS

If the switching period for the power switches is T, then, dT is the on-state period, and (1-d) T is the off-state period, where d is the duty cycle of the power switches. It is assumed that the capacitor voltage and the inductor current are constant during each switching period, and the forward voltage drop and the on-state resistance of the power semiconductors are ignored.

$$\begin{cases} U_{in} \times dT + (U_{in} - U_{C2}) \times (1 - d)T = 0 \\ (U_{C1} + U_{C2}) \times dT + (U_{C2} - U_{C4}) \times (1 - d)T = 0 \end{cases}$$

The voltage relationship between the output and capacitor voltages can be found, in terms of the two switching states which are shown in Fig. 3:

$$\begin{cases} U_{c1} = U_{c2} \\ U_{c3} = U_{c5} = U_{c2} + U_{c4} \\ U_{o} = U_{c4} + U_{c5} \end{cases}$$

As a result, the output voltage *U*o can be obtained from above equations as follows:

$$U_{\rm o} = \frac{3+d}{(1-d)^2} U_{\rm in} = M \times U_{\rm in}$$



converter

where *M* is the conversion ratio, i.e. the voltage gain, from the above equation shows that the proposed converter can theoretically obtain a high and wide voltage gain range. The voltage gain as a function of the duty cycle for the proposed converter has been compared to the converters in and and these are shown in Fig. 5. It can be concluded that the voltage gain of the proposed converter is higher than the converters in below reference papers, especially when d>0.2. Although the converter has a better voltage gain curve, the low conversion efficiency and the non-common ground will cause more power losses and increased du/dt issues, which



will be analyzed in Table I. Considering the voltage gain, the efficiency and the common ground together, the proposed converter in this paper has the advantages of a high and wide voltage gain range, an acceptable conversion efficiency, and a common ground.



Fig -5: Comparisons of voltage gain as a function of the duty cycle for different converter topologies.

5. SIMULATION RESULTS





6. HARDWARE RESULTS



7. CONCLUSIONS

A high voltage gain DC-DC Boost converter with a wide input range, continuous input current and common ground points between the input side and the load side has been proposed in this paper. The voltage stress across the main power switches is lower than half of the output voltage. In addition, the proposed converter can keep the output voltage at 400V using a voltage control loop, when the input voltage changes from 80V to 40V. Therefore, it is suitable for the power interface between a fuel cell source and the DC bus for the motor drive in fuel cell vehicles.

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